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SUPPLEMENTAL REISSUE DECLARATION

To The Honorable Commissioner of Patents and Trademarks:

Sir:

The undersigned are applicants of the Reissue Application Serial No. 07/985,141, filed December 3, 1992 for Reissue of Letters of Patent for GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED DATA TRANSFER TO REDUCE MEMORY SIZE, United States Patent No. 4,975,857, granted to them December 4, 1990, of which Hitachi Limited, whose post office address is Tokyo, Japan, is now sole owner by Assignment recorded on April 5, 1989 at Reel 5061 and Frame 277, and on whose behalf and with whose assent the Reissue application is made, hereby reaffirm their offer to surrender said Letters Patent. However, said Letters Patent apparently has been lost. A Declaration under 37 CFR §1.178 stating that said Letters Patent has not been found was filed on March 23, 1994.

The undersigned also reaffirm their appointment of the following attorneys as principal attorneys in this application:

Donald R. Antonelli, Reg. No. 20,296;
David T. Terry, Reg. No. 20,178;
Melvin Kraus, Reg. No. 22,466;
William I. Solomon, Reg. No. 28,565;
Gregory E. Montone, Reg. No. 28,141;
Ronald J. Shore, Reg. No. 28,577;
Donald E. Stout, Reg. No. 26,422;
Alan E. Schiavelli, Reg. No. 32,087;
James N. Dresser, Reg. No. 22,937;
Carl I. Brundidge, Reg. No. 29,621 and
Paul J. Skwierawski, Reg. No. 32,173

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GROUP 2/00

Please direct all communications to the following address:

Antonelli, Terry, Stout & Kraus, LLP
1300 North Seventeenth Street
Suite 1800
Arlington, VA 22209

We, Koyo KATSURA, Shinichi KOJIMA and Noriyuki KURAKAMI, declare that:

We are subjects of Japan residing respectively at Hitachiota-shi; Maebashi-shi and Takasaki-shi, all of Japan;

We verily believe ourselves to be the original first and joint inventors of the invention described and claimed in the United States Letters Patent No. 4,975,857, and in the specification of the Reissue Application for which invention we solicit a Reissue patent;

We do not know and do not believe that said invention was ever known or used in the United States of America before our invention thereof;

We hereby state that we have reviewed and understand the contents of the specification of the Reissue Application, including the claims, the Declaration under 37 CFR §1.178 filed March 23, 1994, the various Supplemental Reissue Declarations filed in response to objections raised by the Examiner and the various Amendments filed in response to various Office Actions up to the time of filing the present Supplemental Reissue Declaration;

We acknowledge the duty to disclose information which is material to the examination of the Reissue Application in

accordance with Title 37, Code of Federal Regulations, Section 1.56(a);

We verily believe the original patent to be wholly or partly inoperative or invalid by defective specification and claiming less and/or more than we had a right to claim in the patent;

Specifically, during review of the specification and claims of U.S. Patent No. 4,975,857, subsequent to its issuance, undertaken as a result of discussions with another party concerning the technology of that party, it was found that the claims thereof were unduly limited in that they called for limitations not necessary to the patentable invention as disclosed.

More particularly it was discovered that an error arose during the prosecution of U.S. application Serial No. 07/302,332 filed January 27, 1989 from which U.S. Patent No. 4,975,857 issued due to the failure of the inventors and that of the Japanese Agent to fully appreciate and recognize that the invention could have been claimed more broadly. Specifically, the error occurred due to the fact that the claims could have been broadened to more simply recite that the invention provides a first bus having M lines interconnecting a memory and a memory controller and a second bus having N lines interconnecting data processor and the memory controller, wherein N and M are integers and N is greater than M, and that the memory controller transfers M bits of data to and from the memory in a time shared fashion

and transfers N bits of data in parallel to and from the data processor. U.S. Patent No. 4,975,857 was insufficient in that it did not claim the above-described subject matter.

All of the errors, including the above-described error, being corrected in the Reissue Application up to the time of filing the present Supplemental Reissue Declaration arose without any deceptive intention on our part.

We hereby claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Japanese Patent Application No. 63-93448, filed April 18, 1988, in Japan.

We further declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

April 16, 1998
Date


Koyo KATSURA

Post Office Address:

April 27, 1998
Date
Post Office Address:

Shinichi Kojima
Shinichi KOJIMA

May 11, 1998
Date
Post Office Address:

Noriyuki Kurakami
Noriyuki KURAKAMI

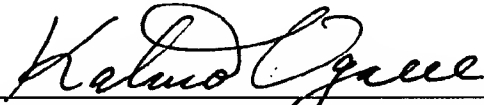
CONSENT OF ASSIGNEE

The undersigned assignee of the entire interest in the above-mentioned Letters Patent No. 4,975,857 as evidence by the Assignment recorded at Reel 5061 and Frame 277 hereby assent to Reissue Application Serial No. 07/985,141, filed December 3, 1992.

HITACHI, LTD.

1998-5-25

Date



Katsuo OGAWA, Patent Attorney
General Manager, Patent Dept.